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LAW DEPARTMENT
7700 WEST PARMER LANE MD:TX32/PL02
AUSTIN, TX 78729

02/07/2006 TBESHAH2 00000017 503079 10807624

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Stacie Herrera

(Depositor's name)

Stacie Herrera

(Signature)

February 6, 2006

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/807,624	03/24/2004	Helmut Broch	SC13191ZP	4056

TITLE OF INVENTION: TRANSISTOR WITH REDUCED GATE-TO-SOURCE CAPACITANCE AND METHOD THEREFOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	02/17/2006
EXAMINER	ART UNIT		CLASS-SUBCLASS		
QUACH, TUAN N	2826		438-197000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 Daniel D. Hill
 2 James L. Clingan, Jr.
 3

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Freescale Semiconductor, Inc.

Austin, TX

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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Authorized Signature D. D. HillDate 2-6-06Typed or printed name Daniel D. HillRegistration No. 35,895

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DATE: February 6, 2006

TO: MS: ISSUE FEE (571) 272-2885
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USPTO (571) 273-2885
(LOCATION) (FAX NUMBER)

FROM: Stacie Herrera for Daniel D. Hill (512) 996-6848
(SENDER) (EXTENSION)

TOTAL NUMBER OF PAGES 3 (including this page)

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Docket No.: SC13191ZP

Applicant: Helmut Brech

Serial No.: 10/807,624

Art Unit: 2826

Filed: April 24, 2004

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

1.	x	1 page Facsimile Cover Sheet
2.	x	1 page PTOL-85B Issue Fee Transmittal (in duplicate)

Paid by Deposit Account 503079, Freescale Semiconductor, Inc: \$1700

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